**Testing Station 1**

* LB
* LH
* LW
* LBU
* LHU
* SB
* SH
* SW
* ADDI
* SLTI
* SLTIU
* XORI
* ORI
* ANDI
* SLLI
* SRLI
* ADD
* SUB
* XOR
* SRL
* SRA
* OR
* AND
* BEQ

| ADDI x31 x0 1 //for incrementing or shifting by 1  LW x1 0(x0)  LH x2 4(x0)  LB x3 8(x0)  LBU x4 12(x0)  LHU x5 16(x0)  ADD x6 x1 x2  SUB x7 x1 x2  XOR x8 x1 x2  OR x9 x1 x2  AND x10 x1 x2  ADDI x11 x1 4  XORI x12 x1 4  ORI x13 x1 4  ANDI x14 x1 4  SRL x1 x1 x31  SRA x1 x1 x31  SLLI x1 x1 1  SRLI x1 x1 1  BEQ x0 x0 4  ADDI x1 x1 -1  ADDI x1 x1 1  SW x1 0(x0)  SB x1 4(x0)  SH x1 8(x0) | 00000000000100000000111110010011  00000000000000000010000010000011  00000000010000000001000100000011  00000000100000000000000100000011  00000000110000000100001000000011  00000001000000000101001010000011  00000000001000001000001100110011  01000000001000001000001110110011  00000000001000001100010000110011  00000000001000001110010010110011  00000000001000001111010100110011  00000000010000001000010110010011  00000000010000001100011000010011  00000000010000001110011010010011  00000000010000001111011100010011  00000001111100001101000010110011  01000001111100001101000010110011  00000000000100001001000010010011  00000000000100001101000010010011  00000000000000000000001001100011  11111111111100001000000010010011  00000000000100001000000010010011  00000000000100000010000000100011  00000000000100000000001000100011  00000000000100000001010000100011 |
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